



CP 2123  
PATENT

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Application No.: 10/055,241  
Filed: October 29, 2001  
Inventor(s):  
Joseph E. Peck, Matthew  
Novacek, Hugo A. Andrade and  
Newton G. Petersen

Title: SYSTEM AND METHOD  
FOR CONFIGURING A  
RECONFIGURABLE  
SYSTEM

§ Examiner: Unknown  
§ Group/Art Unit: 2123  
§ Atty. Dkt. No: 5150-63500  
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I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to Commissioner for Patents, Office of Initial Patent Examination, Customer Service Center, Alexandria, VA 22313-1450, on the date indicated below.

Jeffrey C. Hood

Name of Registered Representative

Signature

Date

**INFORMATION DISCLOSURE STATEMENT**

**Mail Stop Amendment**

Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

Sir:

Applicant requests consideration of ☒ the references listed on the attached Form PTO-1449 and/or ☐ the additional information identified below in paragraph 3. A copy of each reference listed on the Form PTO-1449 is enclosed.

1. This Information Disclosure Statement is submitted:

- a. ☐ within 3 months of the filing date of a national application other than a continued prosecution application under § 1.53(d);  
☐ within 3 months of the date of entry of the national stage as set forth in § 1.491 in an International application;  
☒ before the mailing date of a first Office Action on the merits; or  
☐ before the mailing of a first Office Action after the filing of a request for continued examination under § 1.114.

- b. ☐ after the events of above paragraph 1a and prior to the mailing date of a final Office Action or Notice of Allowance, and thus: ☐ the certification of paragraph 2 below is provided, or ☐ a fee of \$180.00 is enclosed.
- c. ☐ after the mailing date of a final Office Action or a Notice of Allowance and prior to payment of the issue fee, and thus: the certification of paragraph 2 below is provided and a fee of \$180.00 is enclosed.

2. It is hereby certified:

- ☐ that each item of information contained in this Information Disclosure Statement was cited in a communication from a foreign patent office in a counterpart foreign application not more than three months prior to the filing of the Statement, or
- ☐ that no item of information contained in the Information Disclosure Statement was cited in a communication from a foreign patent office in a counterpart foreign application or, to the knowledge of the person signing the certification after making reasonable inquiry, was known to any individual designated in § 1.56 (c) more than three months prior to the filing of the Statement.

3. ☐ Consideration of the following additional information (including any co-pending or abandoned U.S. applications, prior uses and/or sales, etc.) is requested:

4. For each non-English language reference listed on the attached Form PTO-1449:

- ☐ reference is made to an English language translation submitted herewith, and/or
- ☐ reference is made to a foreign patent office search report (in the English language) submitted herewith, and/or
- ☐ reference is made to an English language translation of a foreign patent office search report submitted herewith, and/or
- ☐ reference is made to the concise explanation contained in the specification of the present application at page(s) \_\_\_\_\_, and/or
- ☐ reference is made to the concise explanation set forth below:

5. ☐ Applicant also offers the following comments for the Examiner's consideration:

6. ☐ Also enclosed is a copy of a foreign search report citing these references.

7. ☐ The listed documents were brought to the attention of the Applicant(s) after payment of the issue fee in the captioned case. The documents were cited in a communication from a foreign patent office in a counterpart foreign application

not more than three months prior to the filing of this Information Disclosure Statement. Applicant(s) request this Information Disclosure Statement and attached Form PTO-1449 be placed in the file of the captioned application.

8. ☐ Applicant(s) request that the Information Disclosure Statement and attached Form PTO-1449 and references, which are being filed before the grant of the patent and pursuant to 37 C.F.R. § 1.97(i), be placed in the file of the captioned application.

Applicant does not believe that any fees are required relating to this Information Disclosure Statement. If any required fees are missing, the Commissioner is authorized to charge said fees to Meyertons, Hood, Kivlin, Kowert & Goetzel, P.C. Deposit Account No. 50-1505/5150-63500/JCH.

Respectfully submitted,



Jeffrey C. Hood  
Reg. No. 35,198  
Attorney for Applicant(s)

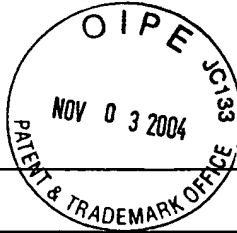
MEYERTONS, HOOD, KIVLIN, KOWERT & GOETZEL, P.C.  
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AUSTIN, TEXAS 78767-0398  
(512) 853-8800

Date: 11/1/2004

**Form PTO-1449** (modified)  
List of Patents and Publications  
For Applicant's Information  
Disclosure Statement  
(Use several sheets if necessary)

ATTY. DOCKET NO: 5150-63500

SERIAL NO: 10/055,241



APPLICANT: Joseph E. Peck

FILING DATE: October 29, 2001

GROUP: 2123

**U.S. PATENT DOCUMENTS**

EXAM. INITIALS	REF. DES	DOCUMENT NUMBER	DATE	NAME	CLASS	SUB CLASS	FILING DATE IF APPROPRIATE

**FOREIGN PATENT DOCUMENTS**

EXAM. INITIALS	REF. DES	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB CLASS	TRANSLATION YES/NO
	C1	DE 692 32 869 T2	Sept. 4, 2003	Germany			YES
	C2	DE 42 05 524 A1	Aug. 27, 1992	Germany			YES

**OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)**

C3	Lauwereins, R; Engels, M; Ade, M; Peperstraette, J; Grape-II: A System-Level Prototyping Environment for DSP Applications, Computer, Vol. 28, Issue 2, pp. 35-43, 2/95.
C4	Lysaght, P; Stockwood, J; A Simulation Tool for Dynamically Reconfigurable Field Programmable Gate Arrays, IEEE Transactions on Very Large Scale Integration Systems, Vol. 4, Issue 3, pp. 381-390, 9/96.
C5	Petronino et al., An FPGA-based Data Acquisition System for a 95 GHz. W-band Radar, IEEE International Conference on Acoustics, Speech and Signal Processing, Vol. 5, pp. 4105-4108, April 1997.
C6	Boulay et al., A High Throughput Controller for a 256-Channel Cardiac Potential Overlapping System, Canadian Conference on Electrical and Computer Engineering, Vol. 1, pp. 539-542, September 1995.

XAMINER:

DATE CONSIDERED:

XAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to the patent owner.

SERIAL NO: 10/058,150



FILING DATE: October 29, 2001

GROUP: 2123

C7	Levy, M., "DSP Design Tools Target FPGAs," <a href="http://www.reed-electronics.com/ednmag/archives/1996/062096/13df2.htm">www.reed-electronics.com/ednmag/archives/1996/062096/13df2.htm</a> , June 20, 1996.
C8	Heimdahl, M.P.E., Keenan, D.J., "Generating Code from Hierarchical State-Based Requirements," Proceedings of IEEE International Symposium on Requirements Engineering (RE'97)," January 1997.
C9	Integrated Systems, Inc., "MATRIXx Product Family Technical Specifications," Product Manual, Copyright 1995.
C10	Pauer, E.K., "Multiprocessor System Development for High Performance Signal Processing Applications," Proceedings of the 1997 IEEE Int. Workshop on Rapid System Prototyping (RSP)," Copyright 1997.
C11	Drusinsky, D., "Extended State Diagrams and Reactive Systems," Dr. Dobb's Journal, October 1994.
C12	Chen, X., Ling, X., Amano, H., "Software Environment for WASMII: a Data Driven Machine with a Virtual Hardware," Proceedings of 4 <sup>th</sup> Workshop on Field-Programmable Logic and Applications (FPL '94), pages 208-219, Springer Verlag, September 1994.

**DATE CONSIDERED:**

Information Disclosure Statement--PTO 1449 (modified)